

## EAST Search History

| Ref # | Hits   | Search Query  | DBs                | Default Operator | Plurals | Time Stamp       |
|-------|--------|---|--------------------|------------------|---------|------------------|
| S1    | 59912  | microcontroller   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 15:14 |
| S2    | 404027 | (configurable adj logic adj array) or<br>cla or cpld or pld or fpga or rom or<br>eeprom or eeprom                                   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 15:15 |
| S3    | 706364 | (instruction or command or code or<br>program) same (load\$4 or stor\$4 or<br>transfer\$4)  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 20:57 |
| S4    | 211679 | S2 same S3  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 15:17 |
| S5    | 16444  | (integrated or soi) same S4   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 15:17 |
| S6    | 8067   | 7?/?\$.ccls. and S5   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 15:18 |
| S7    | 429814 | (instruction or command) same<br>(load\$4 or stor\$4 or transfer\$4)  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:03 |
| S8    | 162765 | (load\$4 or stor\$4 or transfer\$4)<br>near2 (instruction or command)   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 20:58 |
| S9    | 23830  | S8 with execut\$4 near3 (processor<br>or up or microprocessor)  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:04 |
| S10   | 23681  | S8 with execut\$4 near3 (processor<br>or microprocessor)  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:05 |
| S11   | 1783   | S10 same integrated   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:05 |
| S12   | 102    | "711"/\$.ccls. and S11  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:06 |
| S13   | 90940  | (instruction or command) same<br>(load\$4 and (stor\$4 or transfer\$4))   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:04 |
| S14   | 22663  | (instruction or command) near2<br>(load\$4 and (stor\$4 or transfer\$4))  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:04 |
| S15   | 729    | ((instruction or command) near2<br>(load\$4 and (stor\$4 or transfer\$4)))<br>with execut\$4 near3 (processor or<br>microprocessor) | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:05 |
| S16   | 204    | "711"/\$.ccls. and S15  | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:06 |
| S17   | 583039 | soc or (integrated adj circuit)   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:07 |
| S18   | 302    | S15 and S17   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:07 |
| S19   | 89     | S16 and S17   | US-PGPUB;<br>USPAT | ADJ              | ON      | 2007/01/07 21:22 |

## EAST Search History

|     |       |   |                    |     |    |                  |
|-----|-------|---|--------------------|-----|----|------------------|
| S20 | 29743 | loading near3 (data or instructions or command)                   | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:23 |
| S21 | 75339 | executing near3 (data or instructions or command)                 | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:24 |
| S22 | 443   | S20 with S21  | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:24 |
| S23 | 129   | S22 with (processor or cpu)                                       | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:25 |
| S24 | 28168 | (soc or (integrated adj circuit) or (system adj on adj chip)).ti. | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:26 |
| S25 | 0     | S23 and S24   | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:26 |
| S26 | 259   | S20 same S21 same processor                                       | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:26 |
| S27 | 1     | S24 and S26   | US-PGPUB;<br>USPAT | ADJ | ON | 2007/01/07 21:27 |